





TFT COLOR LCD MODULE

NL8048BC19-02C

18cm (7.0 Type) WVGA LVDS interface (1port)

DATA SHEET

DOD-PP-3093 (3rd edition)

This DATA SHEET is updated document from DOD-PP-0820(2).

All information is subject to change without notice. Please confirm the sales representative before starting to design your system.



INTRODUCTION



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Each quality grade is designed for applications described below. Any customer who intends to use a product for application other than that of Standard is required to contact TMJ sales representative in advance.

The **Standard:** Applications as any failure, malfunction or error of the products are free from any damage to death, human bodily injury or other property (Products Safety Issue) and not related the safety of the public (Social Issues), like general electric devices.

Examples: Office equipment, audio and visual equipment, communication equipment, test and measurement equipment, personal electronic equipment, home electronic appliances, car navigation system (with no vehicle control functions), seat entertainment monitor for vehicles and airplanes, fish finder (except marine radar integrated type), PDA, etc.

The **Special:** Applications as any failure, malfunction or error of the products might directly cause any damage to death, human bodily injury or other property (Products Safety Issue) and the safety of the public (Social Issues) and required high level reliability by conventional wisdom.

Examples: Vehicle/train/ship control system, traffic signals system, traffic information control system, air traffic control system, surgery/operation equipment monitor, disaster/crime prevention system, etc.

The **Specific:** Applications as any failure, malfunction or error of the products might severe cause any damage to death, human bodily injury or other property (Products Safety Issue) and the safety of the public (Social Issues) and developed, designed and manufactured in accordance with the standards or quality assurance program designated by the customer who requires extremely high level reliability and quality.

Examples: Aerospace system (except seat entertainment monitor), nuclear control system, life support system, etc.

The quality grade of this product is the "Standard" unless otherwise specified in this document.





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1. OUTLINE

1.1 STRUCTURE AND PRINCIPLE

Color LCD module NL8048BC19-02C is composed of the amorphous silicon thin film transistor liquid crystal display (a-Si TFT LCD) panel structure with driver LSIs for driving the TFT (Thin Film Transistor) array and a backlight.

The a-Si TFT LCD panel structure is injected liquid crystal material into a narrow gap between the TFT array glass substrate and a color-filter glass substrate.

Color (Red, Green, Blue) data signals from a host system (e.g. signal generator, etc.) are modulated into best form for active matrix system by a signal processing board, and sent to the driver LSIs which drive the individual TFT arrays.

The TFT array as an electro-optical switch regulates the amount of transmitted light from the backlight assembly, when it is controlled by data signals. Color images are created by regulating the amount of transmitted light through the TFT array of red, green and blue dots.

1.2 APPLICATION

• For industrial use

1.3 FEATURES

- Adoption of T-EVT (Transmissive- Enhanced View TFT)
- High luminance
- High contrast
- Wide viewing angle
- Low reflection
- LVDS interface
- Reversible-scan direction
- Selectable 8-bit or 6-bit digital signals for data of RGB
- LED backlight
- Replaceable lamp for backlight
- Acquisition product for UL60950-1/CSA-C22.2 No.60950-1-03 (File number: E170632)
- \bullet Compliance with the European RoHS directive (2011/65/EU)



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2. GENERAL SPECIFICATIONS

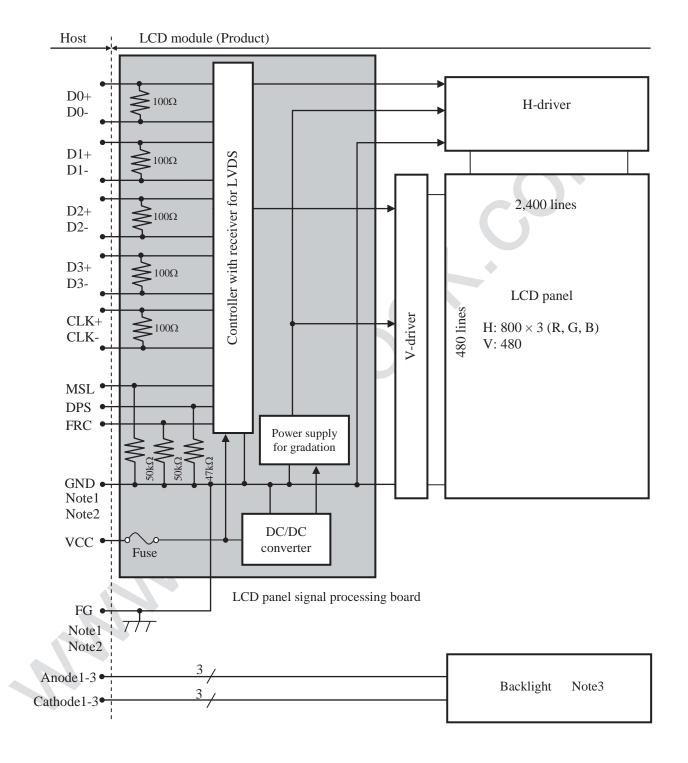
Display area	152.4 (H) × 91.44 (V) mm
Diagonal size of display	18cm (7.0 inches)
Drive system	a-Si TFT active matrix
Display color	16,777,216 colors (At 8-bit input, FRC terminal= High) 262,144 colors (At 6-bit input, FRC terminal= Low or Open)
Pixel	800 (H) × 480 (V) pixels
Pixel arrangement	RGB (Red dot, Green dot, Blue dot) vertical stripe
Dot pitch	0.0635 (H) × 0.1905 (V) mm
Pixel pitch	0.1905 (H) × 0.1905 (V) mm
Module size	170.0 (W) × 111.0 (H) × 8.5 (D) mm (typ.)
Weight	170g (typ.)
Contrast ratio	800:1(typ.)
Viewing angle	At the contrast ratio ≥10:1 • Horizontal: Right side 80° (typ.), Left side 80° (typ.) • Vertical: Up side 80° (typ.), Down side 80° (typ.)
Designed viewing direction	 At DPS= Low or Open: Normal scan Viewing direction without image reversal: Up side (12 o'clock) Viewing direction with contrast peak: Down side (6 o'clock) Viewing angle with optimum grayscale (γ≒2.2): Normal axis (perpendicular)
Polarizer surface	Clear + Antireflection(AR)
Polarizer pencil-hardness	2H (min.) [by JIS K5600]
Color gamut	At LCD panel center 60 % (typ.) [against NTSC color space]
Response time	$Ton+Toff (10\% \longleftrightarrow 90\%)$ 18 ms (typ.)
Luminance	At IL=25 mA/One circuit 550 cd/m ² (typ.)
Signal system	LVDS interface (1port) (Receiver: THC63LVDF84B, THine Electronics Inc. or equivalent) [8-bit/6-bit digital signals for data of RGB colors, Dot clock (CLK), Data enable (DE)]
Power supply voltage	LCD panel signal processing board: 3.3V
Backlight	LED backlight: (Replaceable part • Lamp holder set: 70LHS05
Power consumption	At IL= 25mA/One circuit, Checkered flag pattern 3.4 W (typ.)







3. BLOCK DIAGRAM



Note1: Relation between GND (Signal ground) and FG (Frame ground) in the LCD module is as follows.

GND - FG Connected

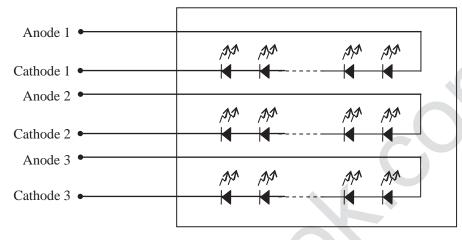
Note2: GND and FG must be connected to customer equipment's ground, and it is recommended that these grounds to be connected together in customer equipment.





Note3: Backlight in detail









4. DETAILED SPECIFICATIONS

4.1 MECHANICAL SPECIFICATIONS

Parameter	Specification		Unit
Module size	$170.0 \pm 0.5 \text{ (W)} \times 111.0 \pm 0.5 \text{ (H)} \times 8.5 \pm 0.5 \text{ (D)}$	Note1	mm
Display area	152.4 (H) × 91.44 (V)	Note1	mm
Weight	170 (typ.), 180 (max.)		g

Note1: See "8. OUTLINE DRAWINGS".

4.2 ABSOLUTE MAXIMUM RATINGS

	Paramete	r	Symbol	Rating	Unit	Remarks
Power supply voltage	LCD panel s	signal processing board	VCC	-0.3 to +4.0	V	
Input voltage	Di	splay signals Note1	VD	-0.3 to VCC+0.3	V	-
for signals	Fu	nction signals Note2	VF	-0.3 to VCC+0.3	V	
It	ncident light in	ntensity	II	150,000	lx	Note3
Backlight	Power dissip	oation	PD	1.1	W	per one circuit
Backlight	Forward cur	rent	IL	35	mA	per one circuit
	Storage tempe	rature	Tst	-30 to +80	°C	-
On anoting ton		Front surface	TopF	-20 to +70	°C	Note4
Operating ten	iperature	Rear surface	TopR	-20 to +70	°C	Note5
				≤ 95	%	Ta ≤ 40°C
	Relative hum	idity	RH	≤ 85	%	40°C <ta≤ 50°c<="" td=""></ta≤>
	Note6		KH	≤ 55	%	50°C <ta≤ 60°c<="" td=""></ta≤>
				≤ 36	%	60°C <ta≤ 70°c<="" td=""></ta≤>
	Absolute hun Note6	nidity	АН	≤ 70 Note7	g/m ³	Ta> 70°C

Note1: D0+/-, D1+/-, D2+/-, D3+/-, CLK+/-.

Note2: DPS, FRC, MSL.

Note3: If the product surface (polarizer) is exposed to an ultraviolet ray, the polarizer may discolor (Surface treatment may be damaged.). Use a filter to protect the polarizer from the ultraviolet

Note4: Measured at LCD panel surface (including self-heat)

Note5: Measured at LCD module's rear shield surface (including self-heat)

Note6: No condensation

Note7: Water amount at $Ta = 70^{\circ}C$ and RH = 36%





4.3 ELECTRICAL CHARACTERISTICS

4.3.1 LCD panel signal processing board

 $(Ta = 25^{\circ}C)$

Parameter		Symbol	min.	typ.	max.	Unit	Remarks
Power supply voltage	;	VCC	3.0	3.3	3.6	V	-
Power supply current		ICC	-	310 Note1	460 Note2	mA	at VCC = 3.3V
Permissible ripple volta	ge	VRP	-	-	100	mVp-p	for VCC
Differential input threshold voltage	High	VTH	-	-	+100	mV	at VCM=1.2V
	oltage Low		-100	-	1	mV	Note3
Terminating resistance	e	RT	-	100	1	Ω	-
Input voltage for	High	VFH	0.7VCC	-	VCC	V	CMOS level
DPS, FRC and MSL signals	Low	VFL	0	-	0.3VCC	V	CWOS level
Input current for	High	IFH	-	-	300	μΑ	
DPS, FRC and MSL signals	Low	IFL	-300	-	-	μΑ	-



Note1: Checkered flag pattern [by IEC61747-6]

Note2: Pattern for maximum current

Note3: Common mode voltage for LVDS receiver



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4.3.2 Backlight

(Ta=25°C, Note1, Note2, Note3)

Parameter	Symbol	min.	typ.	max.	Unit	Remarks
Forward current	IL	-	25.0	30.0	mA	Note4
Forward Voltage	VL	-	29.7	34.2	V	at IL= 25 mA /One circuit

Note1: Please drive with constant current.

Note2: The above specifications are for one LED circuit of the backlight.

Note3: The Luminance uniformity may be changed depending on the current variation between 3 circuits. It is recommended that the current value difference between each circuit is less than 5%.

Note4: See "4.2 ABSOLUTE MAXIMUM RATINGS".

4.3.3 Power supply voltage ripple

This product works, even if the ripple voltage levels are beyond the permissible values as following the table, but there might be noise on the display image.

Power supp	oly voltage	Ripple voltage Note1 (Measure at input terminal of power supply)	Unit
VCC	3.3V	≤ 100	mVp-p

Note1: The permissible ripple voltage includes spike noise.

4.3.4 Fuse

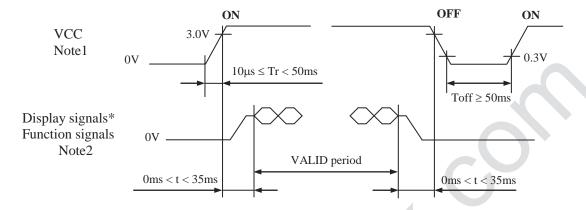
Domomoton		Fuse	Datina	Eusing august	Remarks
Parameter	Type	Supplier	Rating	Fusing current	Remarks
VCC	Nac Page Care K		1.6A	3.2A	Note1
VCC	FCC16162AB	CO., LTD.	32V	3.2A	Note1

Note1: The power supply capacity should be more than the fusing current. If it is less than the fusing current, the fuse may not blow in a short time, and then nasty smell, smoke and so on may occur.



4.4 POWER SUPPLY VOLTAGE SEQUENCE

4.4.1 LCD panel signal processing board



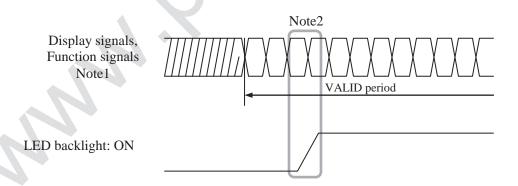
^{*} These signals should be measured at the terminal of 100Ω resistance.

Note1: In terms of voltage variation (voltage drop) while VCC rising edge is below 3.0V, a protection circuit may work, and then this product may not work.

Note2: Display signals (D0+/-, D1+/-, D2+/-, D3+/-, CLK+/-) and function signals (DPS, FRC, MSL) must be Low or High-impedance, exclude the VALID period (See above sequence diagram), in order to avoid that internal circuits is damaged.

If some of display and function signals of this product are cut while this product is working, even if the signal input to it once again, it might not work normally. If customer stops the display and function signals, they should be cut VCC.

4.4.2 LED driver



Note1: These are the display and function signals for LCD panel signal processing board.

Note2: The backlight should be turned on within the VALID period of display and function signals, in order to avoid unstable data display.





4.5 CONNECTIONS AND FUNCTIONS FOR INTERFACE PINS

4.5.1 LCD panel signal processing board

CN1 socket (LCD module side): FI-SE20P-HFE (Japan Aviation Electronics Industry Limited (JAE)) Adaptable plug. (Japan Aviation Electronics Industry Limited (JAE))

Ac	Adaptable plug:		FI-S20S (Japa	es Industry Limite	d (JAE))								
					Input data signal								
P:	in O.	Symbol	Signal	8-	bit	6-bit	Remarks						
				MAP A	MAP B	0-011							
1	A	D3+	Pixel data	R0-R1,G0-G1,B0-B1	R6-R7,G6-G7,B6-B7	-	Note1, Note3						
	В	GND	Ground	-	-	Ground	Note4						
2	A	D3-	Pixel data	R0-R1,G0-G1,B0-B1	R6-R7,G6-G7,B6-B7		Note1, Note3						
	В	GND	Ground	-	-	Ground	Note4						
3	3	DPS	Selection of scan direction	C									
	1	FRC	Selection of the number of colors	Hi	gh	Low or Open	Note1 Note5						
	5	GND	Ground		Ground								
6	5	CLK+	Pixel clock			Note3							
7	7	CLK-	1 IACI CIOCK			Notes							
8	3	GND	Ground		Ground								
Ģ)	D2+	· Pixel data	B4-B7,DE	,DE	Note3							
1	0	D2-	1 ixel data	B4-B7,DE	,DE	Notes							
1	1	GND	Ground		Ground		Note4						
1	2	D1+	Pixel data	G3-G7,B2-B3	G1-G5,E	20 R1	Note3						
1	3	D1-	1 ixel data	U3-U7,B2-B3	G1-G5,E	Ю-В1	Notes						
1	4	GND	Ground		Ground		Note4						
1	5	D0+	Pixel data	R2-R7,G2	DO D5	CO	Note3						
1	6	D0-	1 ixei uata	K2-K/,U2	R0-R5	,00	notes						
1	7	GND	Ground		Ground		Note4						
1	8	MSL	Selection of LVDS input map	Low or Open	High	Low or Open	Note5						
1	9	VCC	Downer grants		Notal								
2	0	VCC	Power supply			Note4							



Note1: See "4.6 DISPLAY COLORS AND INPUT DATA SIGNALS".

Note2: See "4.8 SCANNING DIRECTIONS".

Note3: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.

Note4: All GND and VCC terminals should be used without any non-connected lines.

Note5: See "4.5.4 Connection between receiver and transmitter for LVDS".





4.5.2 Backlight

CN2 socket (LCD module side): DF19G-8P-1H (Hirose Electric Co., Ltd.(HRS)) (Hirose Electric Co., Ltd.(HRS)) Adaptable plug: DF19G-8S-1C(05)

Pin No.	Symbol	Signal	Remarks
1	A1	Anode1	-
2	K1	Cathode1	-
3	A2	Anode2	-
4	K2	Cathode2	-
5	A3	Anode3	-
6	K3	Cathode3	
7	N.C.	-	Keep this pin Open.
8	N.C.	-	Keep this pin Open.

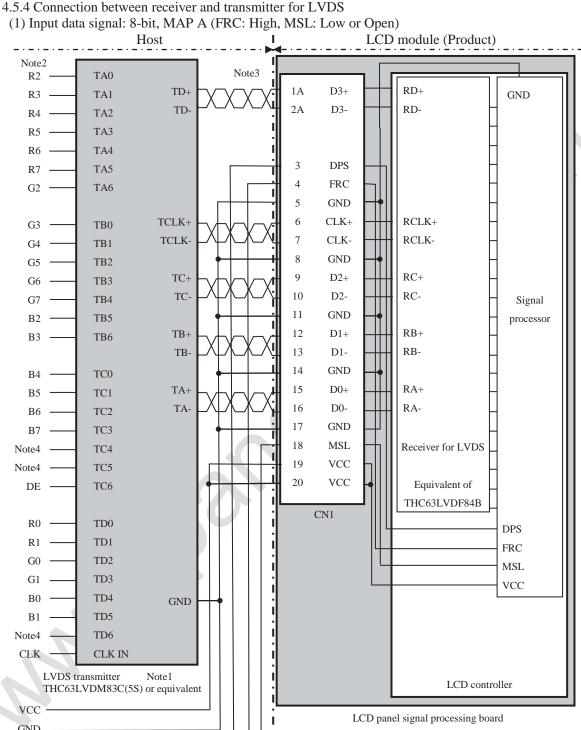
Rear side

4.5.3 Positions of socket





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Note2: LSB (Least Significant Bit) - R0, G0, B0 MSB (Most Significant Bit) - R7, G7, B7

Note3: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.

Note4: Input signals to TC4, TC5 and TD6 are not used inside the product, but do not keep them open to avoid noise problem.



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DPS FRC MSL -



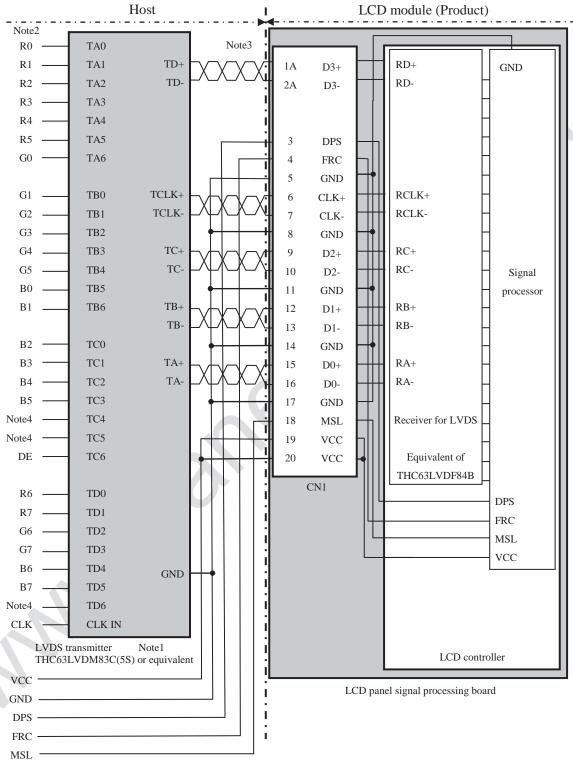


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(2) Input data signal: 8-bit, MAP B (FRC: High, MSL: High)



Note1: Recommended transmitter THC63LVDM83C(5S) (THine Electronics Inc.) or equivalent

Note2: LSB (Least Significant Bit) - R0, G0, B0 MSB (Most Significant Bit) - R7, G7, B7

Note3: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.

Note4: Input signals to TC4, TC5 and TD6 are not used inside the product, but do not keep them open to avoid noise problem.



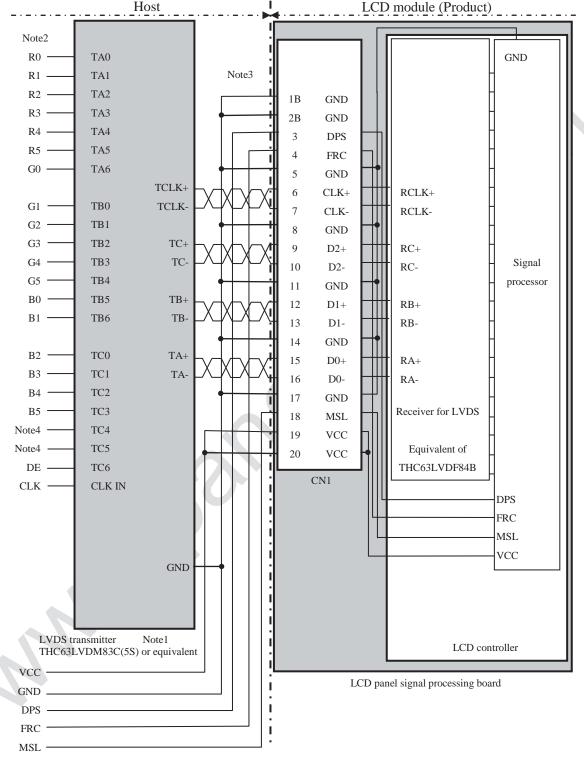


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NL8048BC19-02C





Note1: Recommended transmitter THC63LVDM83RC(5S) (THine Electronics Inc.) or equivalent

Note2: LSB (Least Significant Bit) – R0, G0, B0 MSB (Most Significant Bit) – R5, G5, B5

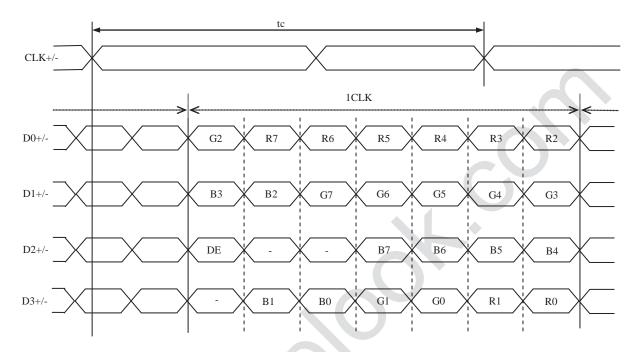
Note3: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.

Note4: Input signals to TC4 and TC5 are not used inside the product, but do not keep them open to avoid noise problem.

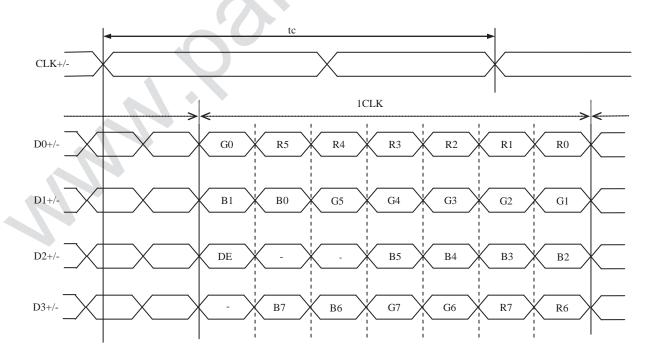


4.5.5 Input data mapping

(1) Input data signal: 8-bit, MAP A



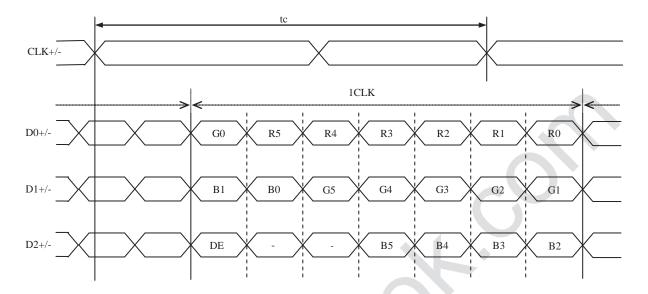
(2) Input data signal: 8-bit, MAP B







(3) Input data signal: 6-bit



4.6 DISPLAY COLORS AND INPUT DATA SIGNALS

4.6.1 Combinations between input data signals, FRC signal and MSL signal

This product can display equivalent of 16,777,216 colors and 262,144 colors scales by combination between input data signals, FRC signal and MSL signal. See following table.

Combination	Input data signals	Input data mapping	CN1- Pin No.1 and 2	FRC terminal	MSL terminal	Display colors	Remarks
1	8-bit	Map A	D3+/-	High	Low or Open	16,777,216	Note1
2	8-bit	Map B	D3+/-	High	High	16,777,216	Note1
3	6-bit	-	GND	Low or Open	Low or Open	262,144	Note2



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Note1: See "4.6.2 16,777,216 colors". Note2: See "4.6.3 262,144 colors".





4.6.2 16,777,216 colors

This product can display equivalent of 16,777,216 colors with 256 gray scales by combination ① and ②.

 $(See \ "\textbf{4.6.1 Combinations between input data signals, FRC signal \ and \ MSL \ signal \ ".)$

Also the relation between display colors and input data signals is as the following table.

Displa	y colors	Data signal (0: Low level, 1: High level)																							
Dispid	19 201013	R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	В7	В6	B5	B4	В3	B2	B1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
lors	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Basic Colors	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
sic	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Ba	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
o o		0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
cal	dark	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ay s	\uparrow					:																:			
Red gray scale	\downarrow					:																:			
Sed	bright	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
lle		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
sca	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
ray	\uparrow					: <								:								:			
Green gray scale	\downarrow													:								:			
iree	bright	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
O		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
o		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
scal	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
ay s	1					:								:								:			
Blue gray scale	\downarrow					:								:								:			
3lue	bright	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
T O		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1





4.6.3 262,144 colors

This product can display 262,144 colors with 64 gray scales by combination ③. (See "4.6.1 Combinations between input data signals, FRC signal and MSL signal ".) Also the relation between display colors and input data signals is as the following table.



Display colors							Data	a sign:	al (0:	Low	level	, 1: H	ligh le	evel)					
Displa	y colors	R 5	R4	R3	R2	R 1	R0	G5	G4	G3	G2	G1	G0	В5	B4	В3	B 2	B 1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
ors	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Basic colors	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
sic	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Ba	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
o.		0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
Red gray scale	dark	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
ay.	↑			:	:												:		
d gr	\			:	:								Ť				:		
Red	bright	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	ъ. т	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ale		0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
y sc	dark	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Green gray scale	↑																		
æn	·	0	0	0	0	0	0	1	1	1	. 1	0	1	0	0	0	. 0	0	0
Gre	bright	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ale	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
y sc	dark	0				U	U	U	U	0		U	U	U	U	U		1	U
Blue gray scale	1																		
ne i	bright	0	0	0	. 0	0	0	0	0	0	. 0	0	0	1	1	1	. 1	0	1
Bl	origin	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1





4.7 DISPLAY POSITIONS

The following table is the coordinates per pixel (See "4.8 SCANNING DIRECTIONS".).

C (0, 0) R G B							
1	T	T	T				
C(0, 0)	C(1, 0)	• • •	C(X, 0)	• • •	C(798, 0)	C(799, 0)	
C(0, 1)	C(1, 1)	• • •	C(X, 1)	• • •	C(798, 1)	C(799, 1)	
•	•	•	•	•	•	•	
•	•	• • •	•	• • •	•	• • •	
•	•	•	•	•	•	•	
C(0, Y)	C(1, Y)	• • •	C(X, Y)	• • •	C(798, Y)	C(799, Y)	
•	•	•	•	•	•)	•	
•	•	• • •	•	• • •	•	•	
•	•	•	•	•	• •	•	
C(0, 478)	C(1, 478)	• • •	C(X, 478)	• • •	C(798, 478)	C(799, 478)	
C(0, 479)	C(1, 479)	• • •	C(X, 479)		C(798, 479)	C(799, 479)	

4.8 SCANNING DIRECTIONS

The following figures are seen from a front view. Also the arrow shows the direction of scan.

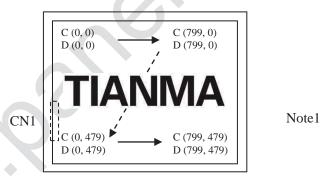


Figure 1. Normal scan (DPS: Low or Open)

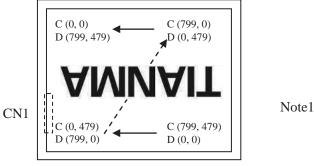


Figure 2. Reverse scan (DPS: High)

Note1: Meaning of C (X, Y) and D (X, Y)

C (X, Y): The coordinates of the display position (See "**4.7 DISPLAY POSITIONS**".)

D (X, Y): The data number of input signal for LCD panel signal processing board

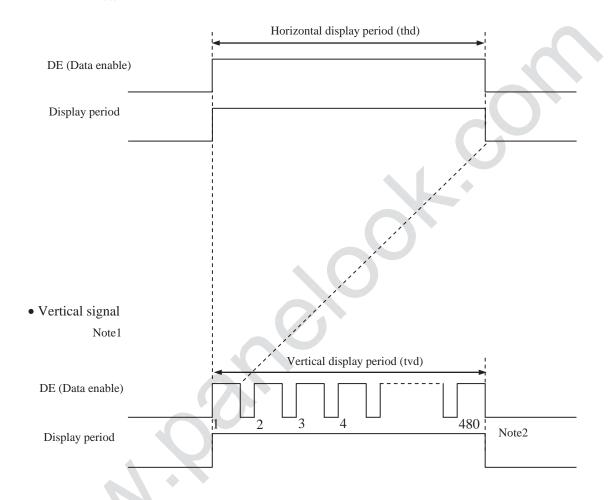




4.9 INPUT SIGNAL TIMINGS

4.9.1 Outline of input signal timings

• Horizontal signal
Note1



Note1: This diagram indicates virtual signal for set up to timing.

Note2: See "4.9.3 Input signal timing chart" for numeration of pulse.





4.9.2 Timing characteristics

(Note1, Note2, Note3)

Parameter			Symbol	min.	typ.	max.	Unit	Remarks	
	Frequency		1/tc	28.0	32.256	36.0	MHz	31.002ns (typ.)	
CLK	Duty ratio		-				-		
	Rise tii	me, Fall time	-	-			ns	-	
	CLK-DATA	Setup time	-				ns		
DATA	CLK-DATA	Hold time	-	-			ns	-	
	Rise time, Fall tin		-				ns		
	Horizontal	Cyrolo	4la	28.44	31.746	36.57	μs	21.5 leHz (tym.)	
		Cycle	th	-	1,024	-	CLK	31.5 kHz (typ.)	
		Display period	thd		800		CLK	-	
		C1-	4	14.931	16.667	19.19	ms	(0,0 H= (tous)	
DE	Vertical (One frame)	Cycle	tv	-	525	-	Н	60.0 Hz (typ.)	
	(One frame)	Display period	tvd		480		Н	-	
	CLK-DE	Setup time	-				ns		
	CLK-DE	Hold time	-		-		ns	-	
	Rise tii	me, Fall time	-				ns		

Note1: Definition of parameters is as follows.

tc = 1CLK, th = 1H

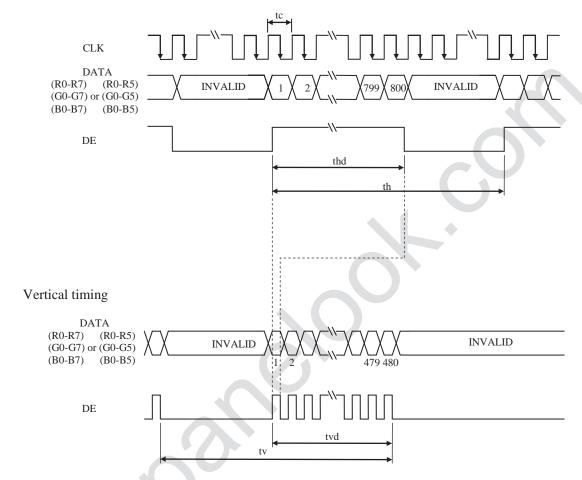
Note2: See the data sheet of LVDS transmitter. Note3: Vertical cycle (tv) should be specified in integral multiple of Horizontal cycle (th).





4.9.3 Input signal timing chart

Horizontal timing







4.10 OPTICS

4.10.1 Optical characteristics

(Note1, Note2)

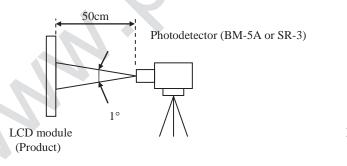
Parameter		Condition	Symbol	min.	typ.	max.	Unit	Measuring instrument	Remarks
Luminand	ce	White at center $\theta R = 0^{\circ}$, $\theta L = 0^{\circ}$, $\theta U = 0^{\circ}$, $\theta D = 0^{\circ}$	L	350	550	-	cd/m ²	BM-5A	-
Contrast ra	ıtio	White/Black at center $\theta R = 0^{\circ}$, $\theta L = 0^{\circ}$, $\theta U = 0^{\circ}$, $\theta D = 0^{\circ}$	CR	500	800	-	-	BM-5A	Note3
Luminance uni	formity	White $\theta R = 0^{\circ}$, $\theta L = 0^{\circ}$, $\theta U = 0^{\circ}$, $\theta D = 0^{\circ}$	LU	-	1.25	1.4	-	BM-5A	Note4
	White	x coordinate	Wx	0.263	0.313	0.363	-		
	white	y coordinate	Wy	0.279	0.329	0.379	-		
	Red	x coordinate	Rx	-	0.623	-			
Chromaticity		y coordinate	y coordinate Ry - 0.355		-	-			
Cilioniaticity	Green	x coordinate	Gx	-	0.318	-	-	SR-3	Note5
		y coordinate	Gy	-	0.589	-	_	SIX-3	
	Blue	x coordinate	Bx	1	0.135	-	-		
	Blue	y coordinate	Ву	-	0.107	-	-		
Color gamut		$\theta R = 0^{\circ}, \theta L = 0^{\circ}, \theta U = 0^{\circ}, \theta D = 0^{\circ}$ at center, against NTSC color space	C	55	60	-	%		
Response ti	ima	White to Black	Ton	-	3	5	ms	BM-5A	Note6
Kesponse ti	ime	Black to White	Toff		15	21	ms	DIVI-JA	Note7
	Right	θU= 0°, θD= 0°, CR≥ 10	θR	70	80	-	0		
V::	Left	θU= 0°, θD= 0°, CR≥ 10	θL	70	80	-	0	EZ	N-4-0
Viewing angle	Up	$\theta R = 0^{\circ}, \ \theta L = 0^{\circ}, \ CR \ge 10$	θU	70	80	-	0	Contrast	Note8
	Down	$\theta R = 0^{\circ}, \ \theta L = 0^{\circ}, \ CR \ge 10$	θD	70	80	-	0	1	

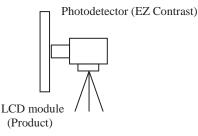
Note1: These are initial characteristics.

Note2: Measurement conditions are as follows.

Ta= 25°C, VCC= 3.3V, IL= 25mA/One circuit, Display mode: WVGA, Horizontal cycle= 1/31.5kHz, Vertical cycle= 1/60.0Hz, DPS= Low or Open: Normal scan

Optical characteristics are measured at luminance saturation after 20minutes from working the product, in the dark room. Also measurement methods are as follows.





Note3: See "4.10.2 Definition of contrast ratio".

Note4: See "4.10.3 Definition of luminance uniformity".

Note5: These coordinates are found on CIE 1931 chromaticity diagram.

Note6: Product surface temperature: TopF= 27°C Note7: See "**4.10.4 Definition of response times**".

Note8: See "4.10.5 Definition of viewing angles".

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4.10.2 Definition of contrast ratio

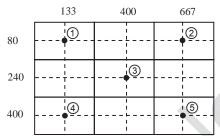
The contrast ratio is calculated by using the following formula.

Contrast ratio (CR) =
$$\frac{\text{Luminance of white screen}}{\text{Luminance of black screen}}$$

4.10.3 Definition of luminance uniformity

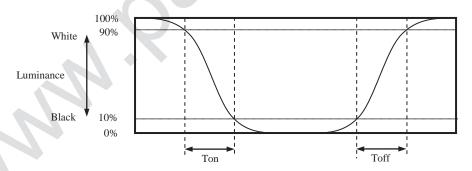
The luminance uniformity is calculated by using following formula.

The luminance is measured at near the 5 points shown below.

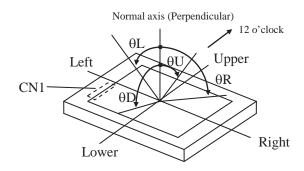


4.10.4 Definition of response times

Response time is measured, the luminance changes from "white" to "black", or "black" to "white" on the same screen point, by photo-detector. Ton is the time it takes the luminance change from 90% down to 10%. Also Toff is the time it takes the luminance change from 10% up to 90% (See the following diagram.).



4.10.5 Definition of viewing angles







5. ESTIMATED LUMINANCE LIFETIME

The luminance lifetime is the time from initial luminance to half-luminance.

This lifetime is the estimated value, and is not guarantee value.

	Condition	Estimated luminance lifetime (Life time expectancy) Note1, Note2, Note3	Unit
LED elementary substance	25°C (Ambient temperature of LED) Continuous operation, IL= 25mA/ One circuit	50,000	h

Note1: Life time expectancy is mean time to half-luminance.

Note2: Estimated luminance lifetime is not the value for LCD module but the value for LED elementary substance.

Note3: By ambient temperature, the lifetime changes particularly. Especially, in case the product works under high temperature environment, the lifetime becomes short.



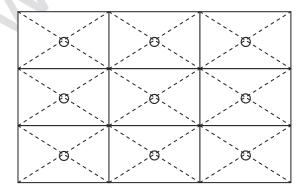


6. RELIABILITY TESTS

Test item	Condition	Judgment Note1			
High temperature and humidity (Operation)	① 60 ± 2°C, RH= 90%, 240hours ② Display data is black.				
High temperature (Operation)	 70 ± 3°C, 240hours Display data is black. 				
Heat cycle (Operation)	 -20 ± 3°C1hour 70 ± 3°C1hour 50cycles, 4 hours/cycle Display data is black. 				
Thermal shock (Non operation)	 30 ± 3°C30minutes 80 ± 3°C30minutes 100cycles, 1hour/cycle Temperature transition time is within 5 minutes. 	No display malfunctions			
ESD (Operation)	 ① 150pF, 150Ω, ±10kV ② 9 places on a panel surface Note2 ③ 10 times each place at 1 sec interval 				
Dust (Operation)	 Sample dust: No. 15 (by JIS-Z8901) 15 seconds stir 8 times repeat at 1 hour interval 				
Vibration (Non operation)	 5 to 100Hz, 19.6m/s² 1 minute/cycle X, Y, Z directions 120 times each direction 	No display malfunctions - No physical damages			
Mechanical shock (Non operation)	① 539m/ s², 11ms ② ±X, ±Y, ±Z directions ③ 5 times each direction				

Note1: Display and appearance are checked under environmental conditions equivalent to the inspection conditions of defect criteria.

Note2: See the following figure for discharge points.







7. PRECAUTIONS

7.1 MEANING OF CAUTION SIGNS

The following caution signs have very important meaning. Be sure to read "7.2 CAUTIONS" and "7.3 ATTENTIONS", after understanding these contents!



This sign has the meaning that customer will be injured by personnel the product will sustain a damage, if customer has wrong operations.



This sign has the meaning that customer will be injured by personnel, if customer has wrong operations.

7.2 CAUTIONS



* Do not shock and press the LCD panel and the backlight! There is a danger of breaking, because they are made of glass. (Shock: Equal to or no greater than 539m/s 2 and equal to or no greater than 11ms, Pressure: Equal to or no greater than 19.6 N (ϕ 16mm jig))

7.3 ATTENTIONS



7.3.1 Handling of the product

- ① Take hold of both ends without touching the circuit board when the product (LCD module) is picked up from inner packing box to avoid broken down or misadjustment, because of stress to mounting parts on the circuit board.
- ② Do not hook nor pull cables, in order to avoid any damage.
- 3 When the product is put on the table temporarily, display surface must be placed downward.
- When handling the product, take the measures of electrostatic discharge with such as earth band, ionic shower and so on, because the product may be damaged by electrostatic.
- ⑤ The torque for product mounting screws must never exceed 0.147N·m. Higher torque might result in distortion of the bezel.
- The product must be installed using mounting holes without undue stress such as bends or twist (See outline drawings). And do not add undue stress to any portion (such as bezel flat area). Bends or twist described above and undue stress to any portion may cause display mura.
- ② Do not press or rub on the sensitive product surface. When cleaning the product surface, wipe it with a soft dry cloth.
- On not push nor pull the interface connectors while the product is working.
- When handling the product, use of an original protection sheet on the product surface (polarizer) is recommended for protection of product surface. Adhesive type protection sheet may change color or characteristics of the polarizer.
- Usually liquid crystals don't leak through the breakage of glasses because of the surface tension of thin layer and the construction of LCD panel. But, if you contact with liquid crystal for the worst, please wash it out with soap.





7.3.2 Environment

- ① Do not operate or store in high temperature, high humidity, dewdrop atmosphere or corrosive gases. Keep the product in packing box with antistatic pouch in room temperature to avoid dusts and sunlight, when storing the product.
- ② In order to prevent dew condensation occurring by temperature difference, the product packing box should be opened after enough time being left under the environment of an unpacking room. Evaluate the leaving time sufficiently because a situation of dew condensation occurring is changed by the environmental temperature and humidity. (Recommended leaving time: 6 hours or more with packing state)
- 3 Do not operate in high magnetic field. Circuit boards may be broken down by it.
- ④ This product is not designed as radiation hardened.

7.3.3 Characteristics

The following items are neither defects nor failures.

- ① Characteristics of the LCD (such as response time, luminance, color uniformity and so on) may be changed depending on ambient temperature. If the product is stored under condition of low temperature for a long time, it may cause display mura. In this case, the product should be operated after enough time being left under condition of operating temperature.
- ② Display mura, flicker, vertical seam or small spot may be observed depending on display patterns.
- 3 Do not display the fixed pattern for a long time because it may cause image sticking. Use a screen saver, if the fixed pattern is displayed on the screen.
- 4 The display color may be changed depending on viewing angle because of the use of condenser sheet in the backlight.
- ⑤ Optical characteristics may be changed depending on input signal timings.
- ⑤ The product gives AR (antireflection) coating of the polarizer surface. Though AR (antireflection) coating actualizes the low reflection with the multilayer structure, the color of reflection may differ between products and the color change of reflection may occur in the same product by fluctuation of AR (antireflection) coating.

7.3.4 Others

- ① All GND and VCC terminals should be used without any non-connected lines.
- ② Do not disassemble a product or adjust variable resistors.
- ③ See "REPLACEMENT MANUAL FOR LAMP HOLDER SET", when replacing lamp holder set.
- 4) Pack the product with original shipping package, in order to avoid any damages during transportation, when returning the product to TMJ for repair and so on.
- (5) The information of China RoHS (II) six hazardous substances or elements in this product is as follows.

China RoHS (II) six hazardous substances or elements								
Lead (Pb)	Mercury (Hg)	Cadmium (Cd)	Hexavalent Chromium (Cr VI)	Polybrominated Biphenys (PBB)	Polybrominated Biphenyl Ethers (PBDE)			
×	0	0	0	0	0			

Note1: (): This indicates that the poisonous or harmful material in all the homogeneous materials for this part is equal or below the limitation level of GB/T26572-2011 standard regulation.

X: This indicates that the poisonous or harmful material in all the homogeneous materials for this part is above the limitation level of GB/T26572-2011 standard regulation.

☆

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